

12/21/01

12-28-01

A

Please type a plus sign (+) inside this box ☐

PTO

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**UTILITY  
PATENT APPLICATION  
TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53 (b))

Attorney Docket No.	01-644 71742
First Inventor	Andreev, Alexander E.
Title	BUILT-IN TEST FOR MULTIPLE MEMORY CIRCUITS
Express Mail Label No.	EL631249015US

**APPLICATION ELEMENTS**  
See MPEP chapter 600 concerning utility patent application

**ADDRESS TO:**  
Assistant Commissioner for Patents  
Box Patent Application  
Washington, DC 20231

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)  
(Submit an original and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.  
See 37 CFR 1.27.
3. ☒ Specification (Total Pages 31)  
(preferred arrangement set forth below)
- Descriptive title of the invention
  - Cross Reference to Related Applications
  - Statement Regarding Fed sponsored R & D
  - Reference to sequence listing, a table, or a computer program listing appendix
  - Background of the invention
  - Brief Summary of the invention
  - Brief Description of the drawings (if filed)
  - Detailed Description
  - Claim(s)
  - Abstract of the disclosure
- ☒ Drawing(s) (35 U.S.C. 113) (Total Sheets 9)
4. Oath or Declaration (Total Pages 5)
- a. ☒ Newly executed (original or copy)
- b. ☐ Copy from a prior application (37 CFR 1.63(d))  
(for continuation/divisional with Box 18 completed)
- i. ☐ **DELETION OF INVENTOR(S)**  
signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
5. ☐ Application Data Sheet. See 37 CFR 1.76

7. ☐ CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)
8. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
- a. ☐ Computer Readable Form (CRF)
- b. Specification Sequence Listing on:
- i. ☐ CD-ROM or CD-R (2 copies); or
- ii. ☐ paper
- c. ☐ Statements verifying identity of above copies

**ACCOMPANYING APPLICATION PARTS**

9. ☒ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement (when there is an assignee) ☒ Power of Attorney
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
13. ☐ Preliminary Amendment
14. ☒ Two (2) Return Receipt Postcards (MPEP 503) (Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)
16. ☒ Nonpublication Request under 35 U.S.C. 122 (b)(2)(B)(i). Applicant must attach form PTO/SB/35 or its equivalent
17. ☒ Other: Cover sheet (1 sheet).....

18. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: \_\_\_\_\_

Prior application Information: Examiner \_\_\_\_\_ Group Art Unit: \_\_\_\_\_

For CONTINUATION or DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

**19. CORRESPONDENCE ADDRESS**

☒ Customer Number or Bar Code Label



24319

or ☐ Correspondence address below

Name	Leo J. Peters LSI Logic Corporation			
Address	1551 McCarthy Blvd., MS D-106			
City	Milpitas	State	CA	Zip Code 95035
Country		Telephone	(408) 433-8708	Fax (408) 433-7770

Name (Print/Type)	Richard E. Wawrzyniak	Registration No. (Attorney/Agent)	36,048
Signature	<i>Richard E. Wawrzyniak</i>	Date	12/21/01

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

# FEE TRANSMITTAL for FY 2002

Patent fees are subject to annual revision.

## Complete if Known

Application Number	New
Filing Date	Herewith
First Named Inventor	Andreev, Alexander E.
Examiner Name	
Group Art Unit	
Attorney Docket No.	01-644 17142

TOTAL AMOUNT OF PAYMENT \$798.00

## METHOD OF PAYMENT

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any overpayment to:

Deposit Account Number: 12-2252

Deposit Account Name: LSI Logic Corporation

☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17

☐ Applicant claims small entity status. See 37 CFR 1.27

2. **Payment Enclosed:**

☐ Check ☐ Credit card ☐ Money Order ☐ Other

## FEE CALCULATION

## 1. BASIC FILING FEE

Large Entity	Small Entity	Fee Code	Fee (\$)	Fee Description	Fee Paid
101	740	201	370	Utility filing fee	\$740.00
106	330	206	165	Design filing fee	
107	510	207	255	Plant filing fee	
108	740	208	370	Reissue filing fee	
114	160	214	80	Provisional filing fee	
SUBTOTAL (1)					\$740.00

## 2. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
21	-20**	1 X \$18	\$18
Independent Claims	2	-3**	0
Multiple Dependent			0 = \$18

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description
103	18	203	9	Claims in excess of 20
102	84	202	42	Independent claims in excess of 3
104	280	204	140	Multiple dependent claim, if not paid
109	84	209	42	**Reissue independent claims over original patent
110	18	210	9	** Reissue claims in excess of 20 and over original patent
SUBTOTAL (2)				\$18.00

\*\*or number previously paid, if greater; For Reissues, see above

## FEE CALCULATION (continued)

Fee Code	Large Entity	Small Entity	Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for ex parte reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	
116	400	216	200	Extension for reply within second month	
117	920	217	460	Extension for reply within third month	
118	1,440	218	720	Extension for reply within fourth month	
128	1,960	228	980	Extension for reply within fifth month	
119	320	219	160	Notice of Appeal	
120	320	220	160	Filing a brief in support of an appeal	
121	280	221	140	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,280	241	640	Petition to revive - unintentional	
142	1,280	242	640	Utility issue fee (or reissue)	
143	460	243	230	Design issue fee	
144	620	244	310	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Processing fee under 37 CFR 1.17(a)	
126	180	126	180	Submission of Information Disclosure Stmt	
581	40	581	40	Recording each patent assignment per property (times number of properties)	\$40.00
146	740	246	370	Filing a submission after final rejection (37 CFR § 1.129(a))	
149	740	249	370	For each additional invention to be examined (37 CFR § 1.129(b))	
179	740	279	370	Request for Continued Examination (RCE)	
169	900	169	900	Request for expedited examination of a design application	
Other fee (specify)					
SUBTOTAL (3)					\$40.00

\*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) \$40.00

## SUBMITTED BY

## Complete (if applicable)

Name (Print/Type)	Richard E. Wawrzyniak	Registration No. (Attorney/Agent)	36,048	Telephone	858-552-1311
Signature	<i>R. E. Wawrzyniak</i>	Date	12/21/01		

**WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization of PTO-2038.**

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

December 21, 2001

Attorney Docket No. 01-644  
71742

Applicant(s): Andreev et al.

Application No.: TBD

Filed: Herewith

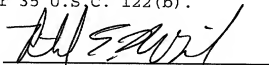
Title: BUILT-IN TEST FOR  
MULTIPLE MEMORY CIRCUITS

**REQUEST AND CERTIFICATION**  
**UNDER 35 U.S.C. 122(b)(2)(B)(i)**

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

12/21/01

Date



Richard E. Wawrzyniak  
Registration No. 36,048

*This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.*

*Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.*

*If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).*

LSI Logic Corporation  
1551 McCarthy Blvd., MS D-106  
Milpitas, CA 95035  
Telephone: (408) 433-8708  
Facsimile: (408) 433-7770